

## Electronic Acknowledgement Receipt

<b>EFS ID:</b>	1794648
<b>Application Number:</b>	10728492
<b>International Application Number:</b>	
<b>Confirmation Number:</b>	9752
<b>Title of Invention:</b>	Low power memory controller with leaded double data rate DRAM package arranged on a two layer printed circuit board
<b>First Named Inventor/Applicant Name:</b>	Eric Hung
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<b>Attorney Docket Number:</b>	03-1237 (5201-27600)
<b>Receipt Date:</b>	21-MAY-2007
<b>Filing Date:</b>	05-DEC-2003
<b>Time Stamp:</b>	15:15:14
<b>Application Type:</b>	Utility

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RAM confirmation Number	6168

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## File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1	Information Disclosure Statement (IDS) Filed	US_IDS_Form__SB_08a.pdf	662262	no	4

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2	NPL Documents	JEDEC_standard.pdf	8100807	no	85
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3	NPL Documents	Bakoglu_pg_321.pdf	116315	no	3
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### Information:

4	Fee Worksheet (PTO-06)	fee-info.pdf	8239	no	2
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Total Files Size (in bytes):			8887623		
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